

Triple Cathode Buffer Layers Composed of PCBM, C₆₀, and LiF for High-Performance Planar Perovskite Solar Cells

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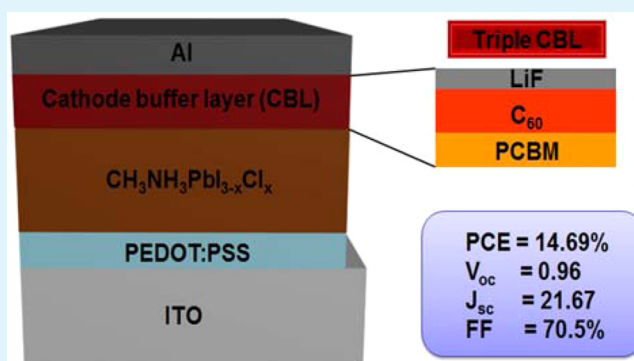
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S Supporting Information

ABSTRACT: In this paper, triple cathode buffer layers (CBLs) composed of phenyl-C₆₁-butyric acid methyl ester (PCBM), C₆₀, and LiF layers were introduced into the planar p-i-n perovskite solar cells (p-i-n PSCs) with a device structure of ITO/PEDOT:PSS/CH₃NH₃PbI_{3-x}Cl_x/CBLs/Al. For comparison, a single CBL of PCBM and a double CBL of PCBM/LiF were also investigated in the p-i-n PSCs. On the basis of the PCBM buffer layer, the addition of a thin LiF layer facilitated the charge collection process and led to the dramatic improvement of the power conversion efficiency (PCE) of the PSCs up to 14.69% under an illumination of AM 1.5G, 100 mW/cm², which is to date one of the highest efficiencies of the p-i-n PSCs. By further insertion of a C₆₀ layer between PCBM and LiF in the triple CBLs, a PCE of 14.24% was obtained, and more importantly, the PCBM/C₆₀/LiF triple CBLs are very helpful for improving the stability of the devices and making the LiF layer less thickness-sensitive for achieving high performances of the p-i-n PSCs.

KEYWORDS: planar perovskite solar cells, cathode buffer layers, PCBM/C₆₀/LiF triple CBLs, CH₃NH₃PbI_{3-x}Cl_x, stability



1. INTRODUCTION

Organic-inorganic hybrid perovskite materials, for application in solar cells, have attracted considerable attention recently because of their unique properties, such as high absorption coefficient, excellent ambipolar charge mobility, and small exciton binding energy.¹⁻¹⁵ Over the past 5 years, the power conversion efficiency (PCE) of organometal halide perovskite solar cells (PSCs) has skyrocketed from 3.8% to 20.1%, approaching the efficiency of commercialized crystalline Si solar cells.^{1,2,16-30} Although first implemented in dye-sensitized solar cells based on mesoporous structures, perovskites have also been applied to planar heterojunction solar cells, which are adaptable to low-temperature solution processes.^{1,14,15,31-34} The configuration of the planar heterojunction solar cell based on perovskite is quite similar to that of organic photovoltaic cells.³⁵⁻³⁹

According to the indium-tin oxide (ITO) electrode used as the anode or cathode, we can define the planar PSCs with ITO as the anode as p-i-n PSCs and that with ITO as the cathode as n-i-p PSCs.³⁴ The n-i-p PSCs usually employing compact TiO₂ as the cathode buffer layer (CBL) have demonstrated rather high performance.⁴⁰ However, the compact TiO₂ layer requires high-temperature sintering at 450–500 °C. Moreover, the n-i-p PSCs show an anomalous current-voltage (*I*-*V*)

hysteresis phenomenon depending on the scan direction and scan rate.⁴¹⁻⁴⁵ As an alternative of n-i-p PSCs, the p-i-n PSCs can be fabricated at relatively low temperature, and the hysteresis problem can also be solved. Recently, special attention has been given to the p-i-n PSCs. Nevertheless, the efficiency of p-i-n PSCs is still lower than that of n-i-p PSCs. Also, as a common problem of both types of PSCs, the device stability is of crucial importance for the researchers.

For the planar heterojunction PSCs, apart from the perovskite photoactive layer, the buffer layers sandwiched between the photoactive layer and the electrodes are crucial to obtaining the highly efficient and stable solar cells.^{46,47} LiF is a widely used CBL in polymer solar cells between the photoactive layer and the Al cathode.⁴⁸ Very recently, Seo et al.¹¹ introduced a very thin layer of LiF (0.5 nm) into the p-i-n PSC based on CH₃NH₃PbI₃ and gained a PCE as high as 14.1%. Ouyang et al. also realized the efficiency enhancement of planar PSCs by adding zwitterion/LiF double interlayers for electron collection.⁴⁹ LiF is known as an electrical insulator, and the thickness is usually controlled to less than 2 nm in

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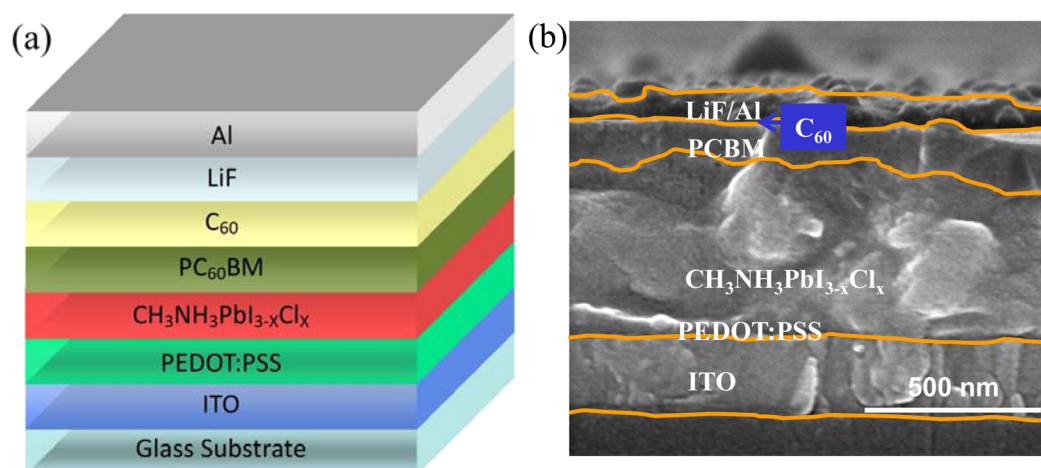


Figure 1. (a) Schematic illustration and (b) cross-sectional SEM image of the PSC. The thicknesses of PEDOT:PSS, $\text{CH}_3\text{NH}_3\text{PbI}_{3-x}\text{Cl}_x$, and PCBM are approximately 40, 450, and 100 nm, respectively.

polymer solar cells for an optimal performance. Compared with the roughness of the perovskite, such a thickness is rather small and very difficult to precisely control during thermal deposition. A tolerant thickness would be necessary for practical applications.

Gao et al. and Liu et al. reported that the C_{60} :LiF composite deposited by coevaporation⁵⁰ or the stacks of C_{60} /LiF prepared via layer-by-layer deposition⁵¹ can greatly enhance the stability of conventional polymer solar cells based on a P3HT:PCBM blend. Inspired by their work, we introduced both of the C_{60} and LiF buffer layers into the p-i-n PSCs, with a device architecture of ITO/PEDOT:PSS/ $\text{CH}_3\text{NH}_3\text{PbI}_{3-x}\text{Cl}_x$ /PCBM/ C_{60} /LiF/Al [where PEDOT:PSS is poly(3,4-ethylenedioxythiophene):poly(styrenesulfonic acid)]. Including the PCBM buffer layer, PCBM/ C_{60} /LiF makes up triple CBLs in this work. It was found that the champion PCE of the p-i-n PSC with PCBM/LiF (1 nm) double CBLs reached 14.69%, which is slightly higher than the PCE reported in ref 11. Also, the p-i-n PSC with triple CBLs of PCBM/ C_{60} (15 nm)/LiF (1 nm) demonstrated a PCE of 14.24%. More importantly, with the triple CBLs, the stability of the p-i-n PSC was dramatically improved. 80% of the PCE remained over 19 days of storage in the glovebox (including 5 min air exposure) for the device with triple CBLs versus 12% remaining over 15 days for the device with PCBM/LiF double CBLs. Moreover, by the introduction of a C_{60} buffer layer, the thickness of LiF needs not to be so critically thin. Even when the thickness of LiF was increased to 5 nm, a PCE of 13.77% was still obtained, which is very important for the fabrication and device performance reproduction of PSCs.

2. EXPERIMENTAL SECTION

Preparation of the Perovskite Precursor. Methylammonium iodide (MAI) was synthesized according to the literature by stoichiometrically reacting methylamine with hydroiodic acid at 0 °C with stirring for 2 h.⁹ The precipitate was washed by dissolving in a minimum amount of ethanol and then adding in a large amount of diethyl ether. The precipitate was collected by filtration. The procedure was repeated three times to get pure MAI. The resulting product (white powder) was dried at 60 °C. The perovskite precursor was prepared by mixing MAI and PbCl_2 (99.999%, Alfa) in a molar ratio of 3:1 in anhydrous *N,N*-dimethylformamide (DMF; 99.8%, Acros), and the final concentration of the perovskite was controlled to approximately 40 wt %. To the mixture was added approximately 1% 1,8-diiodooctane (DIO; volume fraction of DMF). The mixture was then stirred overnight at 60 °C and filtered through a 0.45 μm filter

right before device fabrication. The perovskite precursor was stored in a glovebox.

Fabrication of Planar p-i-n PSCs. As shown in Figure 1, the typical device configuration was ITO/PEDOT:PSS/ $\text{CH}_3\text{NH}_3\text{PbI}_{3-x}\text{Cl}_x$ /CBLs/Al, where the CBLs were PCBM (single CBL), PCBM/LiF (double CBLs), and PCBM/ C_{60} /LiF (triple CBLs). The experimental details are illustrated as follows. ITO-coated glass (CSG Holding Co., Ltd., 10 Ω/sq) substrates were cleaned sequentially with detergent, deionized water, acetone, ethanol, and isopropyl alcohol under sonification for 10 min each and then treated with oxygen plasma for 15 min to generate a hydrophilic surface. The PEDOT:PSS solution (Clevious P VP Al 4083) was filtered and spin-coated onto the cleaned ITO substrates at 2000 rpm for 50 s, and then the ITO substrates were baked at 150 °C for 20 min in air. The following operation was carried out in a glovebox filled with nitrogen. Every time before use, the 40 wt % $\text{CH}_3\text{NH}_3\text{PbI}_{3-x}\text{Cl}_x$ /DIO precursor solution was filtered by running through a 0.45 μm filter and then spin-coated on top of PEDOT:PSS at 3000 rpm for 50 s. The ITO substrates with the perovskite precursor were thermally annealed at 105 °C for 38 min. PCBM with a concentration of 15 mg/mL in chloroform was then spin-coated atop the perovskite layer at 1200 rpm for 60 s. Finally, C_{60} , LiF, and Al electrode were sequentially deposited by thermal evaporation at a base pressure of 1×10^{-4} Pa. The deposition rate and film thickness were monitored with a quartz crystal sensor. A shadow mask was closely covered on the sample to define an active area of 0.2 cm \times 0.2 cm during Al deposition.

Characterization. The current density–voltage (J – V) characteristics were measured using a Keithley 2400 source meter unit under simulated Air Mass 1.5 Global (AM 1.5G) solar illumination with an intensity of 100 mW/cm^2 . The solar illumination was calibrated by a standard Si solar cell. The measurements were conducted inside the glovebox (both O_2 and $\text{H}_2\text{O} < 1$ ppm). The incident photon-to-current conversion efficiency (IPCE) was measured by a solar cell spectral response measurement system (Enli Technology Co., Ltd., QE-R3011). The light intensity was calibrated using a single-crystal Si photovoltaic cell as a reference. The alternating-current (ac) impedance spectroscopy (ACIS) measurements were performed on a IM6 electrochemical workstation (Zahner Zennium, Germany) in dark conditions, with an applied bias of 0.95 V. An ac signal with a root-mean-square (rms) amplitude of 10 mV over the frequency range of 4 MHz to 0.3 Hz was adopted during the measurement. The impedance parameters were determined by fitting the impedance spectra using *Z-view* software. During evaluation of the stability, the PSCs were stored and measured in the glovebox, while the cells were taken out of the glovebox and exposed to ambient atmosphere for about 5 min during the ACIS measurements. Atomic force microscopy (AFM) images were captured on a Multimode 8 microscope (Bruker, Santa Barbara, CA) with peak force quantitative nanomechanical mode.

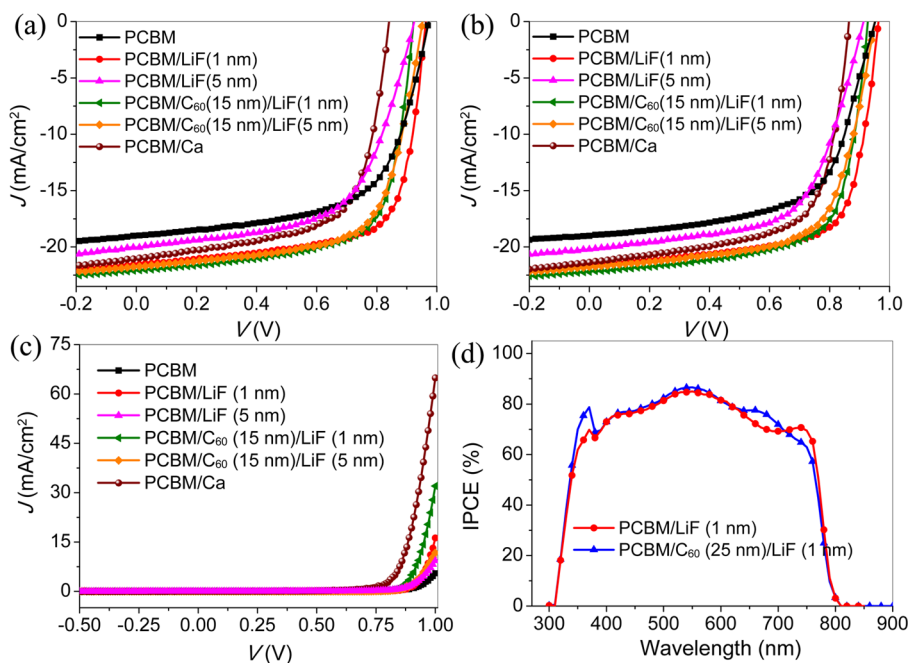


Figure 2. (a) FS and (b) RS J - V curves of the p-i-n PSCs with different CBLs under an illumination of AM 1.5G, 100 mW/cm². (c) J - V characteristics measured in the dark. (d) IPCE spectra of the PSCs with PCBM/LiF (1 nm) and PCBM/C₆₀(25 nm)/LiF (1 nm) as CBLs.

Scanning electron microscopy (SEM) observations were performed on a S-4700 microscope (Hitachi, Japan) operating at 15 kV. X-ray diffraction (XRD) was carried out using an X'Pert-Pro MRD diffractometer (Panalytical).

3. RESULTS AND DISCUSSION

3.1. Photovoltaic Performance of the p-i-n PSCs.

First, we were wondering if the p-i-n PSCs also have hysteresis behavior like the n-i-p PSCs reported in the literature. In order to unravel this question, the J - V curves were recorded by alternating the scan directions. For convenience of description, scanning the bias from low voltage to high voltage is denoted by forward scan (FS), and that from high voltage to low voltage is called reverse scan (RS). The J - V curves of the PSCs with different CBLs were measured with a 200 ms delay after each 10 mV voltage step. The results are shown in Figure 2a,b. The corresponding photovoltaic parameters of the devices are listed in Table 1. It is clearly shown by the two plots, as well as the photovoltaic parameters, that the PCEs obtained from FS and RS are quite close to each other for the different PSCs with different CBLs. The results indicate that the photovoltaic performance data are more reliable for the p-i-n PSCs. In the following, the photovoltaic parameters mentioned in the discussion are obtained from RS.

It can be seen from Table 1 that the PCE of the PSC with a PCBM single CBL is 11.20%. After the introduction of a LiF (1 nm) layer between PCBM and the Al cathode in the device with PCBM/LiF double CBLs, both the short-circuit current density (J_{sc}) and the fill factor (FF) increased dramatically and, as a consequence, the PCEs reached as high as 14.69%. To further confirm the above results, the J - V curves of the device with PCBM/LiF(1 nm) CBLs were measured with various delay times from 5 to 500 ms after each 10 mV voltage step (see the J - V curves in Figure S1 in the Supporting Information, SI). The J - V curves, from either FS or RS, are independent of the delay times, indicating efficient charge collection within the perovskite layer. To find out the reasons

Table 1. Photovoltaic Performance of the p-i-n PSCs with Different CBLs

	CBL	J_{sc} (mA/cm ²)	V_{oc} (V)	FF (%)	PCE (%)	R_s (Ω cm ²)	R_{sh} (Ω cm ²)
FS	PCBM	19.04	0.97	62.2	11.52	8.12	333.72
	PCBM/LiF (1 nm)	21.60	0.96	70.5	14.65	3.15	367.57
	PCBM/LiF (5 nm)	20.04	0.92	59.7	11.07	8.85	290.49
	PCBM/C ₆₀ (15 nm)/ LiF (1 nm)	22.08	0.92	69.2	14.09	3.12	357.79
	PCBM/C ₆₀ (15 nm)/ LiF (5 nm)	21.74	0.95	66.6	13.79	5.68	370.13
	PCBM/Ca	21.03	0.84	65.0	11.50	3.75	245.59
RS	PCBM	19.01	0.95	61.9	11.20	9.61	329.33
	PCBM/LiF (1 nm)	21.67	0.96	70.5	14.69	3.28	378.24
	PCBM/LiF (5 nm)	20.19	0.91	61.5	11.33	9.91	306.00
	PCBM/C ₆₀ (15 nm)/ LiF (1 nm)	22.21	0.93	69.2	14.24	3.14	368.53
	PCBM/C ₆₀ (15 nm)/ LiF (5 nm)	21.76	0.95	66.6	13.77	5.94	372.46
	PCBM/Ca	21.36	0.86	66.6	12.30	3.09	269.62

for the increase of J_{sc} and FF, the series resistances (R_s) and shunt resistances (R_{sh}) were calculated from the slope of the photo J - V curves at 0 mA/cm² and 0 V, respectively. The results are also listed in Table 1. The decrease of R_s and the increase of R_{sh} after insertion of LiF (1 nm) can explain the dramatic improvement of J_{sc} and FF. The decreased R_s can also be verified by the dark J - V characteristics of the devices, as shown in Figure 2c. The relatively larger injection current at positive bias (e.g., 1.0 V) of the device with LiF (1 nm) CBL agrees well with the lower R_s . The formation of a good ohmic contact between PCBM and Al electrode due to the strong dipole moment of LiF should be responsible for the significant reduction of R_s . With regard to this aspect, further discussions are conducted by ACIS later in this paper.

The slight increase of R_{sh} can be explained by the nature of LiF, which is known as an insulator with a wide band gap (\sim 12 eV). This makes LiF a good hole-blocking layer to reduce the current leakage in the device. For the same reason, further

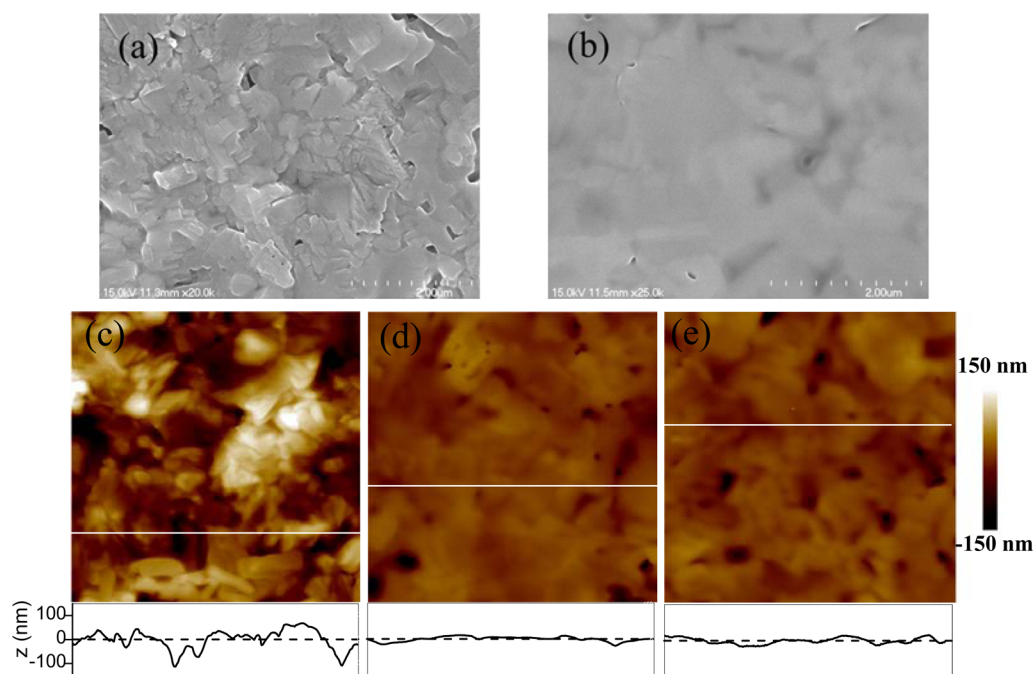


Figure 3. Top-view SEM images of (a) the $\text{CH}_3\text{NH}_3\text{PbI}_{3-x}\text{Cl}_x$ film and (b) the $\text{PCBM}@\text{CH}_3\text{NH}_3\text{PbI}_{3-x}\text{Cl}_x$ film. AFM height images and corresponding sectional profiles of (c) $\text{CH}_3\text{NH}_3\text{PbI}_{3-x}\text{Cl}_x$, (d) $\text{PCBM}@\text{CH}_3\text{NH}_3\text{PbI}_{3-x}\text{Cl}_x$, and (e) $\text{C}_{60}@\text{PCBM}@\text{CH}_3\text{NH}_3\text{PbI}_{3-x}\text{Cl}_x$. The rms values calculated from parts c–e are 45.7, 17.3, and 17.8 nm, respectively. The scanning range of the AFM images is $5 \mu\text{m} \times 5 \mu\text{m}$.

increasing the thickness of LiF will inevitably lead to an increase of R_s , as well as a decrease of FF. This assertion is echoed by the decrease of the PCEs of the devices with an increase in the thickness of LiF to 5 nm, as shown by the photovoltaic performance in Figure 2a,b. In addition, the variation trend of the dark current density indicated by the dark J - V curves in Figure 2c is also consistent with the device performance.

The insertion of C_{60} between PCBM and LiF [i.e., $\text{PCBM}/\text{C}_{60}$ (25 nm)/LiF (1 nm) triple CBLs] led to a PCE of 13.59%. The value is slightly lower than that of the solar cells with PCBM/LiF (1 nm) double CBL. The decrease of the PCE, after insertion of C_{60} , can be ascribed to the drop of V_{oc} (see Table S1 in the SI). To optimize the performance of the PSCs with $\text{PCBM}/\text{C}_{60}$ /LiF triple CBLs, we fixed the LiF thickness at 1 nm and then varied the thickness of C_{60} from 10 to 15 to 25 nm (the photovoltaic performances are listed in Figure S2a,b in the SI). When the thickness of C_{60} was 15 nm, the PCEs reached the maximum value of 14.24%. This is close to the PCE of the PSC with PCBM/LiF (1 nm) double CBLs. To check the reproducibility of the results, we repeated the experiments on fabricating and characterizing the p–i–n PSCs. Statistical data on a batch of five devices for each type are shown in Table S2 in the SI. The PSCs with $\text{PCBM}/\text{C}_{60}$ (15 nm)/LiF (1 nm) triple CBLs exhibited an average PCE of 13.97% with a standard derivation of 0.3%, indicating very good reproducibility.

Herein, the IPCE spectra were employed to explain J_{sc} of the devices with and without a C_{60} buffer layer. As shown in Figure 2d, although the peak values of the devices with and without a C_{60} interlayer both approach 85% at 550 nm, the shapes of the IPCE curves at 630–780 nm are significantly different. This can be attributed to the optical spacer effect from the C_{60} layer according to the theory proposed by Yip et al.⁵² It is worth noting that the integrated IPCE values are in good agreement with the corresponding J_{sc} obtained from the J - V curves.

As described above in this research, when using PCBM/LiF as the CBL, the thickness of LiF has to be thin (~ 1 nm) for a

high efficiency, and increasing the thickness from 1 to 5 nm resulted in a drastic decrease of the PCE from 14.69% to 11.33%. Here we found that after introducing the C_{60} layer the thickness of LiF is not necessarily thin and allows a relatively wide range of thickness for high efficiencies of the devices. As shown in Table 1, the PSC with $\text{PCBM}/\text{C}_{60}$ (15 nm)/LiF (5 nm) triple CBLs showed a PCE of 13.77%, a moderate decrease of PCE compared with 14.24% but significantly higher than that of the solar cell with PCBM/LiF (5 nm) double CBLs. This improvement in the PCE is mainly attributed to the enhanced J_{sc} and FF, which resulted from reduced R_s due to the introduction of C_{60} . R_s of the device with $\text{PCBM}/\text{C}_{60}$ (15 nm)/LiF (5 nm) triple CBLs was $5.94 \Omega \text{cm}^2$, much lower than that of the device with PCBM/LiF (5 nm) double CBLs. The reduced R_s indicates a better electrical conductivity of the C_{60} /LiF (5 nm) bilayer compared with the single LiF (5 nm) layer. It has been proven that intermixing occurs at the C_{60} /LiF interface when 5-nm-thick LiF was deposited on top of the C_{60} layer.⁵¹ This leads to good electrical conductivity of the C_{60} /LiF (5 nm) bilayer considering the percolation path formed by the C_{60} molecules.

The low-work-function metal Ca is an often used CBL in polymer solar cells. For comparison, Ca was also introduced into the p–i–n PSCs. It turns out that the device with PCBM/Ca double CBLs had a PCE of 12.30%, a little bit higher than that with a PCBM single CBL. As shown in Figure 2a,b, the incorporation of the Ca CBL resulted in significant improvement of J_{sc} and moderate improvement of FF but a drastic decrease of V_{oc} in comparison with the PCBM single CBL device. The enhancement in J_{sc} and FF can be attributed to the reduction of R_s , which facilitates electron transport and collection.

3.2. Morphology Studies. The performance of the PSCs is correlated with the morphology of the active layer, i.e., the crystalline structure of the perovskite. As shown by the top-view SEM image in Figure 3a, $\text{CH}_3\text{NH}_3\text{PbI}_{3-x}\text{Cl}_x$ formed sharply

faceted crystalline domains with sizes of a few hundreds of nanometers, and the nanocrystals were closely connected. Generally, a uniform and highly crystallized film usually promises a high performance of the PSCs.^{9,11} After a PCBM buffer layer was capped (Figure 3b), the surface became rather smooth and homogeneous. To obtain the z-direction information on the films, AFM measurement was carried out, and the images and corresponding section analysis are shown in Figure 3c–e. It can be seen clearly that the topologies of $\text{CH}_3\text{NH}_3\text{PbI}_{3-x}\text{Cl}_x$ and $\text{PCBM}@ \text{CH}_3\text{NH}_3\text{PbI}_{3-x}\text{Cl}_x$ have trends similar to those shown by SEM. The surface roughnesses calculated by the rms of the height data in the scan range of $5 \mu\text{m} \times 5 \mu\text{m}$ were 45.7 and 17.3 nm for $\text{CH}_3\text{NH}_3\text{PbI}_{3-x}\text{Cl}_x$ and $\text{PCBM}@ \text{CH}_3\text{NH}_3\text{PbI}_{3-x}\text{Cl}_x$, respectively. With coverage of PCBM, the surface roughness was significantly decreased, as also was evidenced by the corresponding sectional profiles. The smoother surface suggests a better interfacial contact between PCBM and the top cathode. Additionally, full coverage of PCBM on the perovskite film can block the direct contact between the cathode and the active layer and thus minimize the current leakage.¹¹ Nevertheless, further deposition of C_{60} (15 nm) has very little contribution to the surface topology or roughness. This can be ascribed to the fact that (1) thermal evaporation gives an equal regional thickness of C_{60} and (2) the thickness of the C_{60} layer is too small to affect the total roughness.

3.3. ACIS Studies of the p–i–n PSCs. To further investigate the effect of CBLs on the interfacial resistance of the devices, ACIS of the p–i–n PSCs was measured and analyzed. Figure 4a shows Nyquist plots of the p–i–n PSCs with

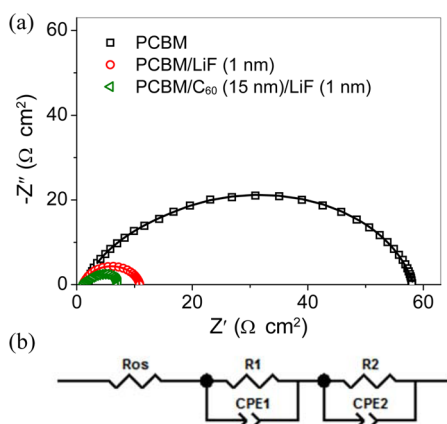


Figure 4. (a) Nyquist plots and fitting curves of the PSCs. ACIS was measured in the dark and near the V_{oc} condition (0.95 V). (b) Equivalent circuit.

different CBLs measured in the dark and near the V_{oc} condition (0.95 V). The impedance data were analyzed by fitting to an equivalent electrical circuit, as shown in Figure 4b. The equivalent circuit consists of a series connection of an ohmic series resistance (R_{os}) and two parallel resistance–constant phase elements ($R||\text{CPE}$). R_{os} is mainly contributed by the conducting wires, contacts, and sheet resistance of the collecting electrodes,⁵³ and CPE is used to describe a nonideal capacitor with the capacitive contribution CPE-T and the quality factor CPE-P.⁵⁴ The parameters obtained by fitting the impedance spectra are listed in Table 2. All of the devices have similar R_{os} , which is reasonable because of the similar configurations of the solar cells. $R_1||\text{CPE}_1$ and $R_2||\text{CPE}_2$ correspond to the semicircles with high and low frequencies

Table 2. Parameters of the Equivalent Circuit for the p–i–n PSCs with Different CBLs

CBL	R_{os} ($\Omega \text{ cm}^2$)	R_1 ($\Omega \text{ cm}^2$)	CPE_{1-T} ($\mu\text{F cm}^{-2}$)	CPE_{1-P}	R_2 ($\Omega \text{ cm}^2$)	CPE_{2-T} ($\mu\text{F cm}^{-2}$)	CPE_{2-P}
PCBM	1.89	5.76	0.17	0.96	50.60	0.46	0.86
PCBM/LiF	1.41	0.31	5.81	1.11	9.14	0.21	0.96
PCBM/ C_{60} /LiF	1.79	1.73	0.07	1.04	3.64	0.14	1.02

and are assigned to the photoactive layer and buffer interlayer, respectively.⁵⁵ R_2 of the p–i–n PSCs with PCBM, PCBM/LiF (1 nm), and PCBM/ C_{60} (15 nm)/LiF (1 nm) CBLs were 50.60, 9.14, and 3.64 $\Omega \text{ cm}^2$, respectively. The variation trend is consistent with that of R_s extracted from the photo J – V curves (Table 1). For a parallel comparison, R_s was reevaluated from the dark J – V curve at around 0.95 V. The values were 18.18, 5.75, and 3.34 $\Omega \text{ cm}^2$. The three different methods all prove that the introduction of the C_{60} layer greatly decreases R_s , which makes a rational explanation for the higher J_{sc} of the corresponding device.

3.4. Stability of the p–i–n PSCs. In addition to the PCE, the stability is another important aspect that needs to be improved for the practical application of PSCs. We found that the insertion of the C_{60} layer is helpful for enhancing the stability of the devices. Figure 5 shows the normalized

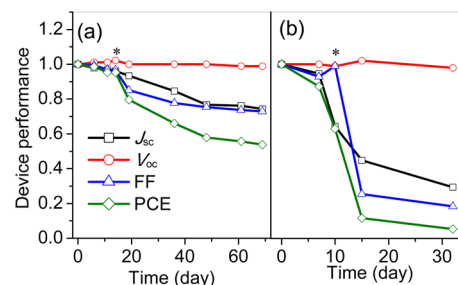


Figure 5. Photovoltaic characteristics normalized by their initial values, for the p–i–n PSCs with (a) PCBM/ C_{60} (15 nm)/LiF (1 nm) and (b) PCBM/LiF (1 nm) CBLs, as a function of the storage time in the glovebox. Asterisks denote the data points of air exposure for ~ 5 min.

photovoltaic characteristics of the devices with PCBM/ C_{60} /LiF triple CBLs and PCBM/LiF double CBLs as a function of the storage time in the glovebox (O_2 and $\text{H}_2\text{O} < 1$ ppm). As shown in Figure 5, the former maintained 95% of its initial efficiency after being stored in the glovebox for 14 days, while the latter retained only 63% after 10 days. Both of them were quite unstable when exposed in air at the data points denoted by asterisks, even for a rather short time. After exposure in air, a drastic decrease of the performance can be observed. It is worth noting that the device with the C_{60} layer is much stable than that without. The stability is also evidenced in the continuous storage after air exposure. With PCBM/LiF double CBLs, merely 12% of the PCE remained over 15 days of storage in the glovebox (including 5 min of air exposure). With triple CBLs, the stability of the p–i–n PSC was dramatically improved. Nearly 80% of the PCE remained over 19 days (including 5 min of air exposure). These results imply that the C_{60} buffer layer may have a blocking effect to water and/or oxygen to some extent, which is greatly helpful to improving the device stability.

From Figure 5, it can be seen that the PCE decay for both devices is dominated by the decline in J_{sc} and FF, while V_{oc}

remains remarkably stable. This phenomenon is especially notable after air exposure for ~ 5 min. To gain more insight into the degradation process, dynamic evolution of the J - V curves as a function of the storage time in the glovebox for both devices with PCBM/ C_{60} /LiF and PCBM/LiF CBLs is presented in Figure S3 in the SI. S-shaped kinks were observed near V_{oc} in the J - V curves for both devices after exposure to air. The kinks became more pronounced with increasing storage time, consequently resulting in decreased J_{sc} and FF. The appearance of the S-shaped kinks implies the existence of large R_s . Considering that no color change was observed for the perovskite films during aging, the significant increase in R_s should not be ascribed to the perovskite decomposition. Therefore, the substantially enhanced R_s most likely originated from the increased contact resistance between the perovskite and the PCBM CBL. The incorporation of the C_{60} layer between the PCBM and LiF layers provides good protection for the perovskite/PCBM interface by blocking water and/or oxygen, which leads to the slower increase of R_s (less severe kink) with the storage time and thus the improvement of the device stability.

4. CONCLUSION

In this study, we found that the performance of the p-i-n PSC, different from the n-i-p PSC, shows a scanning-direction-independent feature. With the introduction of a very thin layer of LiF (1 nm), the performance of the p-i-n PSCs was significantly improved. After insertion of a C_{60} layer (typically 15 nm) between the PCBM and LiF layers, the LiF layer is no longer thickness-sensitive until 5 nm for high device performance. Champion PCEs of 14.24% and 14.69% were obtained for PSCs with PCBM/ C_{60} /LiF triple CBLs and PCBM/LiF double CBLs, respectively. These efficiencies are among the highest PCEs reported to date for the planar p-i-n PSCs based on the device configuration of ITO/PEDOT:PSS/ $CH_3NH_3PbI_{3-x}Cl_x$ /CBLs/Al. More importantly, the incorporation of the C_{60} layer with PCBM and LiF CBLs dramatically enhanced the stability of the p-i-n PSCs. We believe that the results of this study may pave the way toward achieving more efficient and stable p-i-n PSCs, and this idea can be expanded to systems featuring other types of perovskite materials.

■ ASSOCIATED CONTENT

Supporting Information

J - V characteristics of the planar p-i-n PSCs with PCBM/LiF (1 nm) CBLs (Figure S1), effect of the C_{60} layer thickness on the J - V curves and the photovoltaic performance of the p-i-n PSCs with PCBM/ C_{60} /LiF CBLs (Figure S2 and Table S1), average photovoltaic performance parameters of the p-i-n PSCs with different CBLs (Table S2), and evolution of the J - V curves as a function of the storage time in the glovebox for the PSCs with PCBM/ C_{60} (15 nm)/LiF (1 nm) and PCBM/LiF (1 nm) CBLs (Figure S3). This material is available free of charge via the Internet at <http://pubs.acs.org>.

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Notes

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